

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	Examiner: Chung, Phung M.
)	
Inventor(s): Daniel A. Day)	
)	Art Unit: 2117
Patent Application No.: 10/612,293)	
)	
Filed: June 30, 2003)	Confirmation No.: 5819
)	
For: Selective Control of Test-Access Ports in)	
Integrated Circuits)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CHANGE OF CORRESPONDENCE ADDRESS, STATEMENT UNDER 37 CFR 3.73(b), AND
POWER OF ATTORNEY, AND REVOCATION OF PREVIOUS POWERS

Please direct all future correspondence regarding the above-referenced patent application to Blakely, Sokoloff, Taylor & Zafman LLP, Customer Number 76225, 1279 Oakmead Parkway, Sunnyvale, California 94085, and direct all telephone calls to the same at (408) 720-8300.

INTEL CORPORATION

(Name of Assignee of Record)

("assignee of record"), a company organized under the laws of Delaware, having a place of business at 2200 Mission College Blvd, Santa Clara, CA 95052, hereby states that to the best of assignee of record's knowledge and belief it is the assignee of record of the entire right, title, and interest in and to the above-referenced patent application and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee of record. Upon information and belief, the assignment documents that evidence the placement of title in the assignee of record were recorded in the U.S. Patent and Trademark Office. The Assignment was recorded in the Patent and Trademark Office at Reel 014816 Frame 0975.

Pursuant to 37 C.F.R. §§ 1.36 and 3.71, the assignee hereby revokes all powers of attorney previously given and appoints the practitioners associated with Customer Number 76225 as the assignee's respective patent attorneys and patent agents, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Pursuant to 37 C.F.R. § 3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Dated: 02-25-09

Assignee of Interest: Intel Corporation

By: Pam Matlock (Type or Print)

Name: Pam Matlock

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Title: Secretary of Patents

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